



## Tehničke specifikacije

Physical features	
Size(W*H*D)	137×80×62mm
Power dissipation	9W
Memory Features	
Program Memory	24KB , expandable to 72KB
Data Memory	10KB , expandable to 110KB
Super capacitors	about 112 hours (typical)
External battery(optional)	about 200 days (typical)
General Features	
Timers total	256
1ms	4
10ms	16
100ms	236
Counters Total	256( backup by super capacitor)
Internal memory bits	256(backup by super capacitor)
Stored permanently on power down	112
Time interrupts	2 with 1ms resolution
Edge interrupts	4 rising edges and/or 4 falling edges
Analog adjustments	2 with 8 bit resolution
Boolean execution speed	0.15μs
Float execution speed	8μs
Clock	embedded
Integrated Communication Function	
Communication interface	5 3 communication port, PORT0:PPI,RS485 PWL, FPORT0/FPORT1:Freeport RS485
PPI baud rates	9.6, 19.2 and 187.5k
Freeport baud rates	1.2kbaud to 115.2k

Maximum cable length per segment	When it is 1000 m, baud rate is 187.5k; when it is 1200 m, 38.4k.
Isolated repeater applied	
Isolated repeater unapplied	
Maximum number of stations	32 stations per segment,126 stations per network
Maximum number of masters	32
Point to point (PPI Mater Mode)	Yes(NWTR/NETW), 8 total, 2 reserved
I/O Features	
Number of integrated digital inputs	14
Input type	Sink/Source
Number of integrated digital outputs	10
Output type	Solid State----MOSFET, external load common anode connection
Digital I/O map area	256(128 input/128 output)
Analog I/O map area	64(32 input/32 output)
Maximum number of expandable I/O modules	7
Maximum number of digital I/O	230
Maximum number of analog I/O	56AI/28AO
Pulse catch inputs	14
Pulse output	4 axis×200KHz( do not support PLS output in the software package, co-trust software package is a must)
High-speed counters total	6
Single phase counters	6×200KHz
two phase counters	4×200KHz
Digital Input Features	
Number of integrated digital input	14
Input type	Sink/Source(IEC Type 1 sink)
Rated voltage	24V DC ( B series ) 、 5V DC ( A series )
Maximum continuous permissible voltage	30V DC

Surge voltage	35VDC last 0.5s
Logical 1 Signal (minimum)	above 16 VDC except above 15 VD ( I1.1,I1.5 )
Logical 0 Signal (Maximum )	below 8 VDC (except I1.1,I1.5) below 5 VDC ( I1.1,I1.5 )
Isolation( field side and logical circuit)	yes
Optical isolation(Galvanic)	500V AC , 1minute
Isolation group	refer to the Terminal Identification
Simultaneous Input	14
Maximum cable length	
Shielded	500 m(ordinary standard input)
Unshielded	300 m(ordinary standard input)
Digital Output Features	
Number of integrated digital output	10
Output type	Solid state - MOSFET(source ) external load common anode connection
Rated voltage	24V DC
Output range of voltage	5V to 28.8 VDC
Logical 1 Signal (maximum)	0.5V
Logical 0 Signal (minimum)	Vcc minus 0.5 V
Maximum rated current of each point	0.5A
Maximum rated current of each common port	4 A
Leakage current (Max)	10 $\mu$ A
Surge current (Max)	8A , 100ms
Lamp load (Max)	3.5 W
On- state resistance	0.3 ohm typical (Max :0.6 ohm )
Isolation(field side to logical circuit)	yes
Optical isolation(Galvanic)	500V AC , 1 minute
Isolation groups	refer to the Terminal Identification

Delay (Max) Off to on/ on to off	0.2us(Q0.0~ Q0.7) , 50us(Q1.0 and Q1.1)
Pulse frequency(Max)	2 axis : 200KHz(Q0.0~Q0.3)、 1 KHz(Q0.4~Q1.1) 4axis : 200KHz(Q0.0~Q0.7)、 1 KHz(Q0.4~Q1.1)
Simultaneous output	10
Parallel Output	No
Maximum cable length Shielded	500 m(ordinary standard input)
Unshielded	150 m(ordinary standard input)

Odgovarajući odnos između pojedinih IO izlaza funkcije kontrole kretanja i običnih IO izlaza

Ordinary IO	Q0.0	Q0.1	Q0.2	Q0.3	Q0.4	Q0.5	Q0.6	Q0.7	Q1.0	Q1.1
Motion control IO	Pulse_0	Dir_0	Pulse_1	Dir_1	Pulse_2	Dir_2	Pulse_3	Dir_3	Q1.0	Q1.1

Remark :

1、 .Pulse\_0 ----- 0 axis pulse output

Dir\_0 -----0 axis direction output

.Pulse\_1 ----- 1 axis pulse output

Dir\_1 -----1 axis direction output

.Pulse\_2 -----2 axis pulse output

Dir\_2 -----2 axis direction output

.Pulse\_3 -----3 axis direction output

Dir\_3 -----3 axis direction output

2. Q0.0 and Q0.1 do not support PTO and PWM high-speed pulse output in programming soft wares.

3.External load common anode connection is necessary for this CPU DO point.( ordinary Transistor CPUs use external load common cathode connection )

Definition of Communication Port ( 226H-CAN ) 226H

Connector	Pin	PORT0	FPORT0	FPORT1
	1	Shell ground	Shell ground	1 : Shell ground
	2	Logical	Logical	2 : RS485 Signal B/+

	3	RS485 Signal B	RS485 Signal B	3 : RS485Signal A/-
	4	Send request	Send request	
	5	Logical	Logical	
	6	+5V, 100Ω	+5V, 100Ω	
	7	+24V	+24V	
	8	RS485Signal A	RS485Signal A	
	9	Null	10 Agreement options (input)	
	Shell	Shell ground	Shell ground	